

ABSTRACT

An event timing adjustment device that can realize timing adjustment of any event in a simpler and more correct way. The event timing adjustment circuit has multiphase clock generating portion 5 that generates multiphase clocks, and multiphase clock use portion 3. The multiphase clock generating portion 5 generates plural phase clocks having different phases applied for an event input received by input terminal 1. The plural pulse clocks represent plural different adjustment quantities, respectively. Multiphase clock use portion 3 uses any one phase clock among the multiphase clocks, and it generates an event change timing signal representing the changed timing of said event.